

Notice of References Cited	Application/Control No. 10/812,103		Applicant(s)/Patent Under Reexamination SMITH ET AL.	
	Examiner Jonathan R. Plante		Art Unit 2112	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2004/0230414	11-2004	Hoppe et al.	703/014
*	B	US-5,809,283	09-1998	Vaidyanathan et al.	703/16
*	C	US-7,006,960	02-2006	Schaumont et al.	703/15
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
1	U	B. W. Curran, "IBM eServer z900 high-frequency microprocessor technology, circuits, and design methodology" July/September 2002, IBM Journal of Research and Development, Vol. 46, No 4/5, pp 631-644
2	V	Cadence NC-Verilog Tutorial for SimVision Waveform Viewer Users, Cadence Design Systems, Inc., Product Version 3.40, January 2002
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.